

WHAT IS CLAIMED IS:

1. A reception synchronization apparatus for detecting a synchronization timing position thereby determining an effective symbol period, from an OFDM signal each frame of which includes a series of n (integer equal to or greater than 1) time slots and a frame guard period added to the series of n time slots, each time slot including an effective symbol period and a guard period added to the effective symbol period, the reception synchronization apparatus comprising:

a delay circuit for delaying a received OFDM signal by an effective symbol period;

a calculation circuit for calculating the correlation between the received OFDM signal and an OFDM signal delayed by the delay circuit;

a frame guard removing circuit that receives a signal outputted from the calculation circuit, removes a frame guard period from a frame period of the received signal, and outputs a resultant signal;

an interval integrator for cumulatively adding the signal outputted from the frame guard removing circuit, from one time slot period to another; and

a detection circuit for detecting a maximum peak from the interval integration signal obtained for the intervals

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of time slot periods and generating a detection signal indicating a synchronization timing position corresponding to the detected maximum peak position, the detection signal being used to extract the effective symbol period with a precise timing corresponding to the maximum peak position.

2. A reception synchronization apparatus according to Claim 1, further comprising an offset compensation signal generator for generating an offset compensation signal such that a supplied synchronization timing signal indicating the timing of extracting the effective symbol period is compared with the detection signal outputted from the detection circuit to measure a synchronization timing error, and the offset compensation signal is generated depending upon the measured synchronization timing error.

3. A reception synchronization apparatus according to Claim 1, wherein the detection circuit detects a maximum peak by employing guard periods as synchronization points.

4. A reception synchronization apparatus according to Claim 2, wherein the detection circuit detects a maximum peak by employing guard periods as synchronization points.

5. A reception synchronization apparatus according to

Claim 1, wherein the frame guard period is a non-signal period.

6. A reception synchronization apparatus for detecting a synchronization timing position thereby determining an effective symbol period, from an OFDM signal each frame of which includes a series of n (integer equal to or greater than 1) time slots and a frame guard period added to the series of n time slots, each time slot including an effective symbol period and a guard period added to the effective symbol period, the reception synchronization apparatus comprising:

a delay circuit for delaying a received OFDM signal by an effective symbol period;

a calculation circuit for calculating the correlation between the received OFDM signal and an OFDM signal delayed by the delay circuit;

a moving integration circuit for adding the signal outputted from the calculation signal over an entire guard period;

a frame guard removing circuit that receives a signal outputted from the moving integration circuit, removes a frame guard period from a frame period of the received signal, and outputs a resultant signal;

an interval integrator for cumulatively adding the

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signal outputted from the frame guard removing circuit, from one time slot period to another;

and a detection circuit for detecting a maximum peak from the interval integration signal obtained for the intervals of time slot periods and generating a detection signal indicating a synchronization timing position corresponding to the detected maximum peak position, the detection signal being used to extract the effective symbol period with a precise timing corresponding to the maximum peak position.

7. A reception synchronization apparatus according to Claim 6, further comprising an offset compensation signal generator for generating an offset compensation signal such that a supplied synchronization timing signal indicating the timing of extracting the effective symbol period is compared with the detection signal outputted from the detection circuit to measure a synchronization timing error, and the offset compensation signal is generated depending upon the measured synchronization timing error.

8. A reception synchronization apparatus according to Claim 6, wherein the detection circuit detects a maximum peak by employing guard periods as synchronization points.

9. A reception synchronization apparatus according to Claim 7, wherein the detection circuit detects a maximum peak by employing guard periods as synchronization points.

10. A reception synchronization apparatus according to Claim 6, wherein the frame guard period is a non-signal period.

11. A reception synchronization apparatus for detecting a synchronization timing position thereby determining an effective symbol period, from an OFDM signal each frame of which includes a series of n (integer equal to or greater than 1) time slots and a frame guard period added to the series of n time slots, each time slot including an effective symbol period and a guard period added to the effective symbol period, the reception synchronization apparatus comprising:

a delay circuit for delaying a received OFDM signal by an effective symbol period;

a calculation circuit for calculating the correlation between the received OFDM signal and an OFDM signal delayed by the delay circuit;

n frame guard removing circuits, disposed in correspondence with signals in respective first to n th time slot periods, which receive a signal outputted from the

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calculation circuit, remove the frame guard period from the received signal, and output a resultant signal;

n interval integration circuits, disposed in correspondence with the respective n frame guard removing circuit, which cumulatively add the signals outputted from corresponding frame guard removing circuits from one time slot period to another; and

a detection circuit for detecting a maximum peak from the interval integration signals supplied from the respective n interval integration circuits and generating a detection signal indicating a synchronization timing position corresponding to the detected maximum peak position, the detection signal being used to extract the effective symbol period with a precise timing corresponding to the maximum peak position.

12. A reception synchronization apparatus according to Claim 11, further comprising an offset compensation signal generator for generating an offset compensation signal such that a supplied synchronization timing signal indicating the timing of extracting the effective symbol period is compared with the detection signal outputted from the detection circuit to measure a synchronization timing error, and the offset compensation signal is generated depending upon the measured synchronization timing error.

13. A reception synchronization apparatus according to Claim 11, wherein the detection circuit detects a maximum peak by employing guard periods as synchronization points.

14. A reception synchronization apparatus according to Claim 12, wherein the detection circuit detects a maximum peak by employing guard periods as synchronization points.

15. A reception synchronization apparatus according to Claim 11, wherein the frame guard period is a non-signal period.

16. A reception synchronization apparatus for detecting a synchronization timing position thereby determining an effective symbol period, from an OFDM signal each frame of which includes a series of n (integer equal to or greater than 1) time slots and a frame guard period added to the series of n time slots, each time slot including an effective symbol period and a guard period added to the effective symbol period, the reception synchronization apparatus comprising:

a delay circuit for delaying a received OFDM signal by an effective symbol period;

a calculation circuit for calculating the correlation

between the received OFDM signal and an OFDM signal delayed by the delay circuit;

a moving integration circuit for adding the signal outputted from the calculation signal over an entire guard period;

n frame guard removing circuits, disposed in correspondence with signals in respective first to nth time slot periods, which respectively receive a signal outputted from the moving integration circuit, remove the frame guard period from the received signal, and output a resultant signal;

n interval integration circuits, disposed in correspondence with the respective n frame guard removing circuit, which cumulatively add the signals outputted from corresponding frame guard removing circuits from one time slot period to another; and

a detection circuit for detecting a maximum peak from the interval integration signals supplied from the respective n interval integration circuits and generating a detection signal indicating a synchronization timing position corresponding to the detected maximum peak position, the detection signal being used to extract the effective symbol period with a precise timing corresponding to the maximum peak position.

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17. A reception synchronization apparatus according to Claim 16, further comprising an offset compensation signal generator for generating an offset compensation signal such that a supplied synchronization timing signal indicating the timing of extracting the effective symbol period is compared with the detection signal outputted from the detection circuit to measure a synchronization timing error, and the offset compensation signal is generated depending upon the measured synchronization timing error.

18. A reception synchronization apparatus according to Claim 16, wherein the detection circuit detects a maximum peak by employing guard periods as synchronization points.

19. A reception synchronization apparatus according to Claim 17, wherein the detection circuit detects a maximum peak by employing guard periods as synchronization points.

20. A reception synchronization apparatus according to Claim 16, wherein the frame guard period is a non-signal period.

21. A demodulating apparatus for extracting effective symbol periods from an OFDM signal in synchronization with a synchronization timing signal and demodulating signals in

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the extracted effective symbol periods, each frame of the OFDM signal including a series of n (integer equal to or greater than 1) time slots and a frame guard period added to the series of n time slots, each time slot including an effective symbol period and a guard period added to the effective symbol period, the demodulating apparatus comprising:

a timing controller for generating the synchronization timing signal and correcting the synchronization timing on the basis of a supplied offset compensation signal; and

a reception synchronization apparatus comprising

a delay circuit for delaying a received OFDM signal by an effective symbol period;

a calculation circuit for calculating the correlation between the received OFDM signal and an OFDM signal delayed by the delay circuit;

a frame guard removing circuit that receives a signal outputted from the calculation circuit, removes a frame guard period from a frame period of the received signal, and outputs a resultant signal;

an interval integrator for cumulatively adding the signal outputted from the frame guard removing circuit, from one time slot period to another;

a detection circuit for detecting a maximum peak from the interval integration signal obtained for the

intervals of time slot periods and generating a detection signal indicating a synchronization timing position corresponding to the detected maximum peak position, the detection signal being used to extract the effective symbol period with a precise timing corresponding to the maximum peak position; and

an offset compensation signal generator for generating the offset compensation signal to be supplied to the timing controller such that the synchronization timing signal is compared with the detection signal outputted from the detection circuit to measure a synchronization timing error and the measured result is supplied as the offset compensation signal to the timing controller.

22. A demodulating apparatus according to Claim 21, wherein the detection circuit of the reception synchronization apparatus detects a maximum peak by employing guard periods as synchronization points.

23. A demodulating apparatus for extracting effective symbol periods from an OFDM signal in synchronization with a synchronization timing signal and demodulating signals in the extracted effective symbol periods, each frame of the OFDM signal including a series of n (integer equal to or greater than 1) time slots and a frame guard period added to

the series of n time slots, each time slot including an effective symbol period and a guard period added to the effective symbol period, the demodulating apparatus comprising:

a timing controller for generating the synchronization timing signal and correcting the synchronization timing on the basis of a supplied offset compensation signal; and

a reception synchronization apparatus comprising:

a delay circuit for delaying a received OFDM signal by an effective symbol period;

a calculation circuit for calculating the correlation between the received OFDM signal and an OFDM signal delayed by the delay circuit;

a moving integration circuit for adding the signal outputted from the calculation signal over an entire guard period;

a frame guard removing circuit that receives a signal outputted from the moving integration circuit, removes a frame guard period from a frame period of the received signal, and outputs a resultant signal;

an interval integrator for cumulatively adding the signal outputted from the frame guard removing circuit, from one time slot period to another;

a detection circuit for detecting a maximum peak from the interval integration signal obtained for the

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intervals of time slot periods and generating a detection signal indicating a synchronization timing position corresponding to the detected maximum peak position, the detection signal being used to extract the effective symbol period with a precise timing corresponding to the maximum peak position; and

an offset compensation signal generator for generating the offset compensation signal to be supplied to the timing controller such that the synchronization timing signal is compared with the detection signal outputted from the detection circuit to measure a synchronization timing error and the measured result is supplied as the offset compensation signal to the timing controller.

24. A demodulating apparatus according to Claim 23, wherein the detection circuit of the reception synchronization apparatus detects a maximum peak by employing guard periods as synchronization points.

25. A demodulating apparatus for extracting effective symbol periods from an OFDM signal in synchronization with a synchronization timing signal and demodulating signals in the extracted effective symbol periods, each frame of the OFDM signal including a series of n (integer equal to or greater than 1) time slots and a frame guard period added to

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the series of n time slots, each time slot including an effective symbol period and a guard period added to the effective symbol period, the demodulating apparatus comprising:

a timing controller for generating the synchronization timing signal and correcting the synchronization timing on the basis of a supplied offset compensation signal; and

a reception synchronization apparatus comprising:

a delay circuit for delaying a received OFDM signal by an effective symbol period;

a calculation circuit for calculating the correlation between the received OFDM signal and an OFDM signal delayed by the delay circuit;

n frame guard removing circuits, disposed in correspondence with signals in respective first to n th time slot periods, which receive a signal outputted from the calculation circuit, remove the frame guard period from the received signal, and output a resultant signal;

n interval integration circuits, disposed in correspondence with the respective n frame guard removing circuit, which cumulatively add the signals outputted from corresponding frame guard removing circuits from one time slot period to another;

a detection circuit for detecting a maximum peak from the interval integration signals supplied from the

the series of n time slots, each time slot including an effective symbol period and a guard period added to the effective symbol period, the demodulating apparatus comprising:

- a timing controller for generating the synchronization timing signal and correcting the synchronization timing on the basis of a supplied offset compensation signal; and

- a reception synchronization apparatus comprising:

 - a delay circuit for delaying a received OFDM signal by an effective symbol period;

 - a calculation circuit for calculating the correlation between the received OFDM signal and an OFDM signal delayed by the delay circuit;

 - a moving integration circuit for adding the signal outputted from the calculation signal over an entire guard period;

- n frame guard removing circuits, disposed in correspondence with signals in respective first to n th time slot periods, which respectively receive a signal outputted from the moving integration circuit, remove the frame guard period from the received signal, and output a resultant signal;

- n interval integration circuits, disposed in correspondence with the respective n frame guard removing circuit, which cumulatively add the signals outputted from

corresponding frame guard removing circuits from one time slot period to another;

a detection circuit for detecting a maximum peak from the interval integration signals supplied from the respective n interval integration circuits and generating a detection signal indicating a synchronization timing position corresponding to the detected maximum peak position, the detection signal being used to extract the effective symbol period with a precise timing corresponding to the maximum peak position; and

an offset compensation signal generator for generating the offset compensation signal to be supplied to the timing controller such that the synchronization timing signal is compared with the detection signal outputted from the detection circuit to measure a synchronization timing error and the measured result is supplied as the offset compensation signal to the timing controller.

28. A demodulating apparatus according to Claim 27, wherein the detection circuit of the reception synchronization apparatus detects a maximum peak by employing guard periods as synchronization points.

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